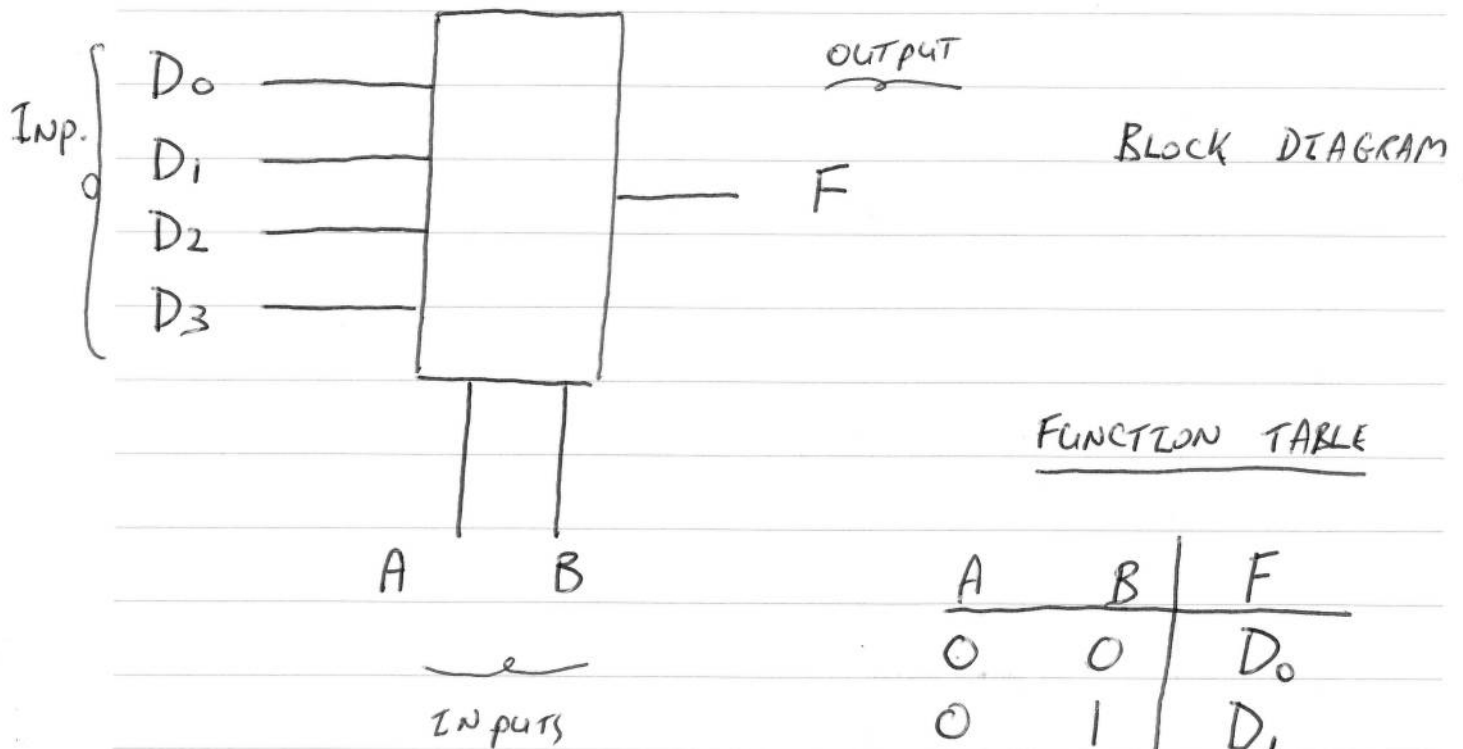


(i)

LAST, WE LOOKED AT 2x1 MULTIPLEXER.  
CAN ALSO HAVE 4x1 " "  
8x1 "

### 4 x 1 MULTIPLEXER

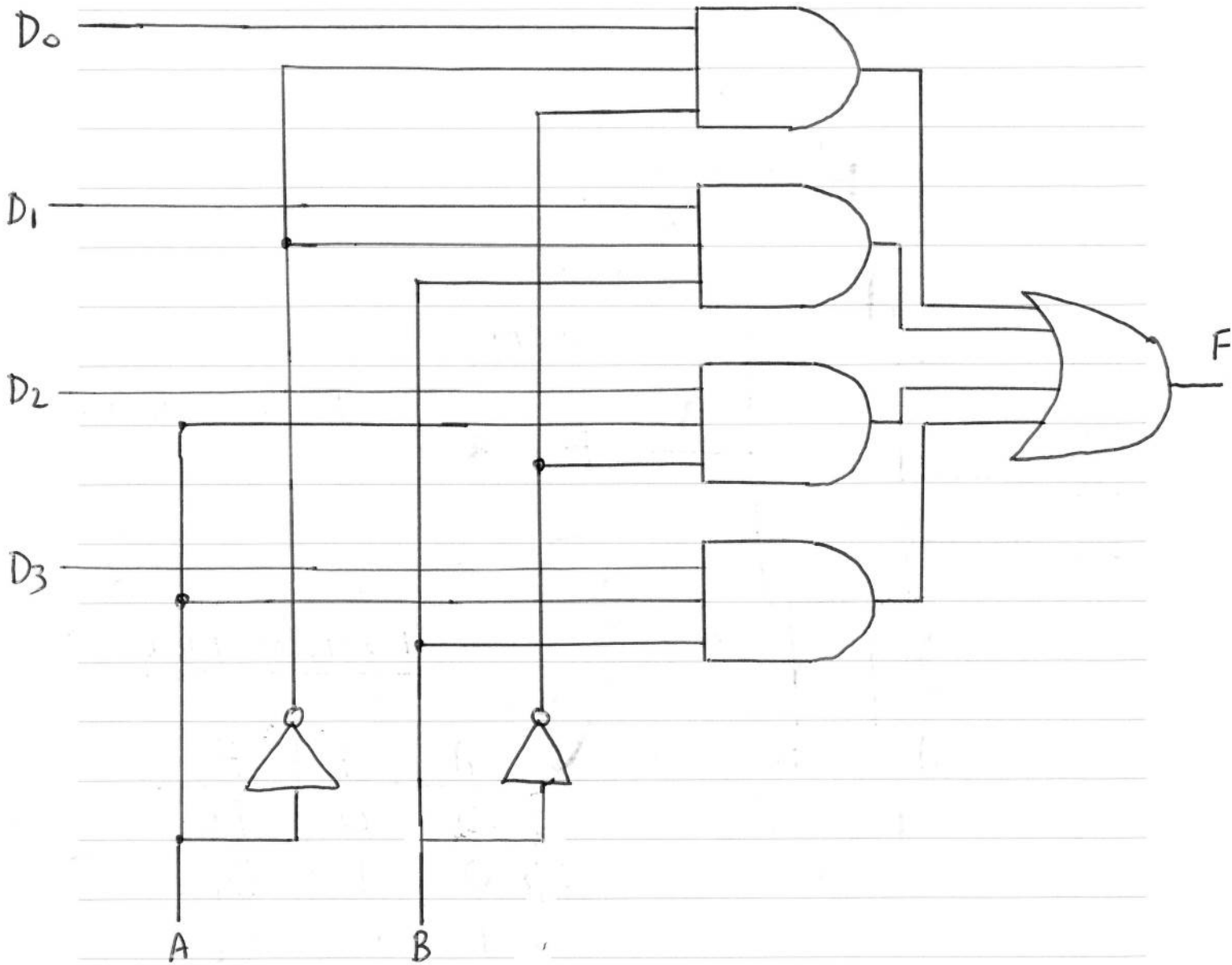


FUNCTION TABLE

A	B	F
0	0	$D_0$
0	1	$D_1$
1	0	$D_2$
1	1	$D_3$

$$F = \bar{A}\bar{B}D_0 + \bar{A}BD_1 + A\bar{B}D_2 + ABD_3$$

(2)



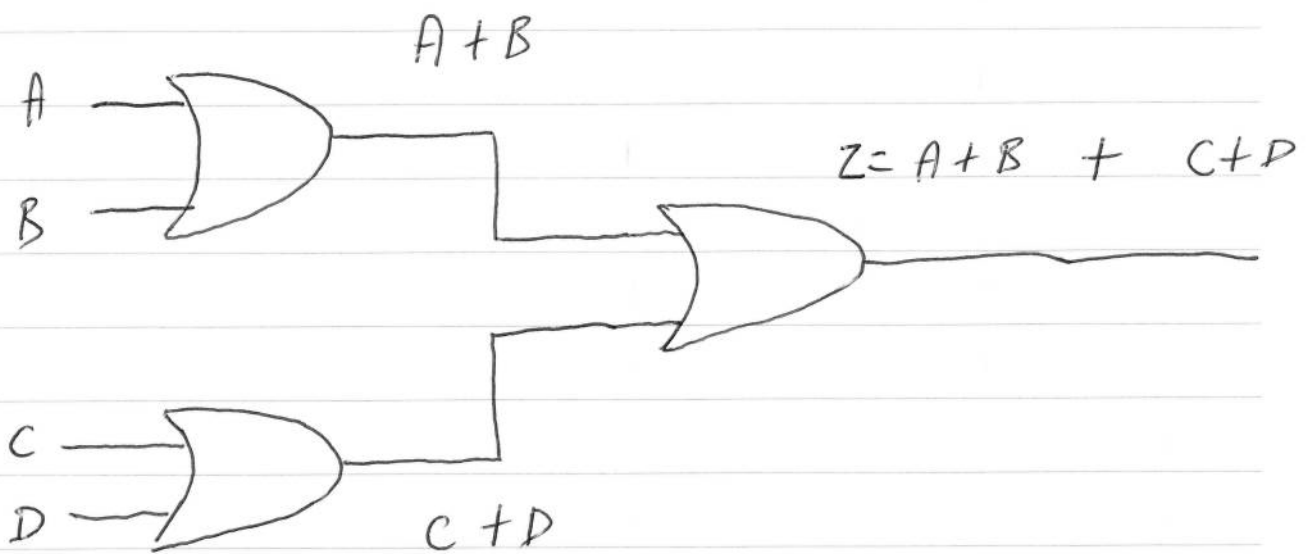
(3)

TRUTH TABLE 4    INPUT    OR

V	W	X	Y	Z
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	1
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	1
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1

④

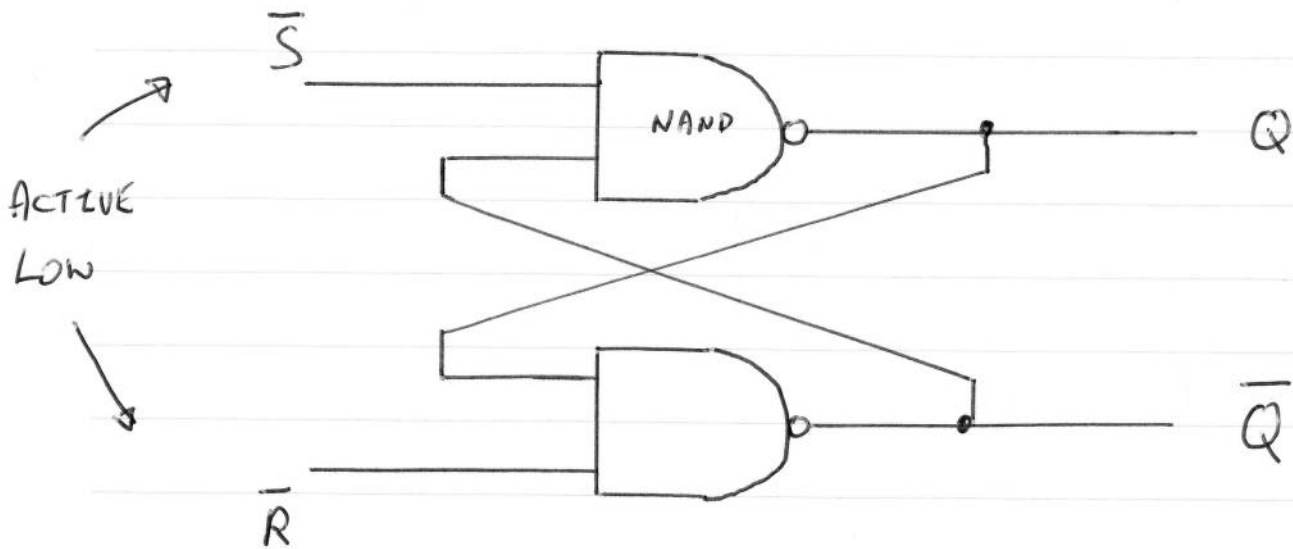
How would you implement  
A 4 input OR using 2 input  
OR GATES?



5

# S-R LATCH

- USES FEEDBACK



## NAND

A	B	Z
0	0	1
0	1	1
1	0	1
1	1	0

## TRUTH TABLE S-R LATCH

$\bar{S}$	$\bar{R}$	Q	$\bar{Q}$
0	0	INVALID	
0	1	1	0
1	0	0	1
1	1	(NO CHANGE) MEMORY STATE	

(6)

PURPOSE: STORE A SINGLE BIT: 0 OR 1

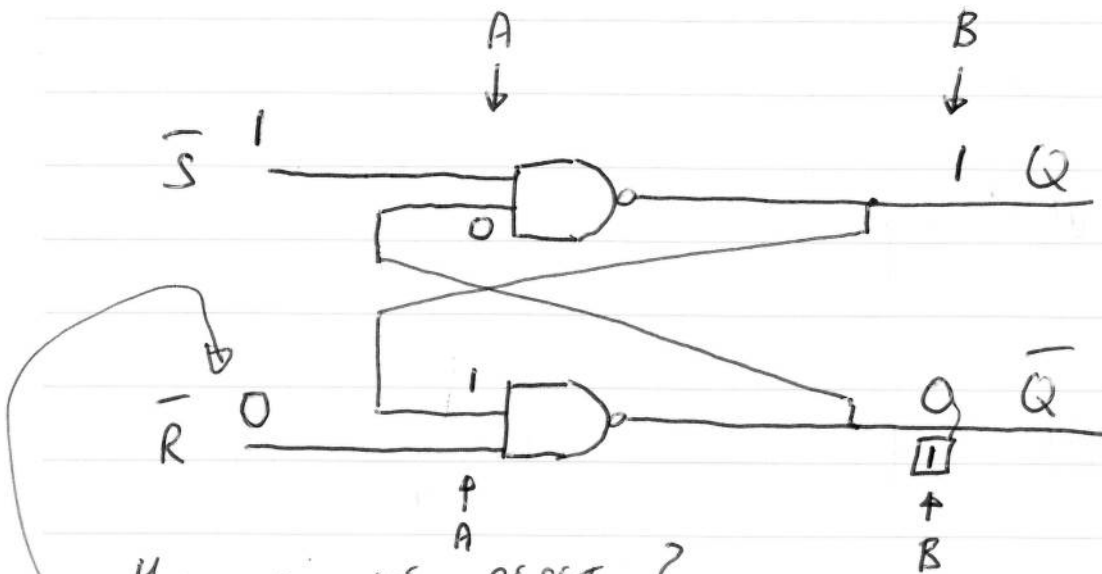
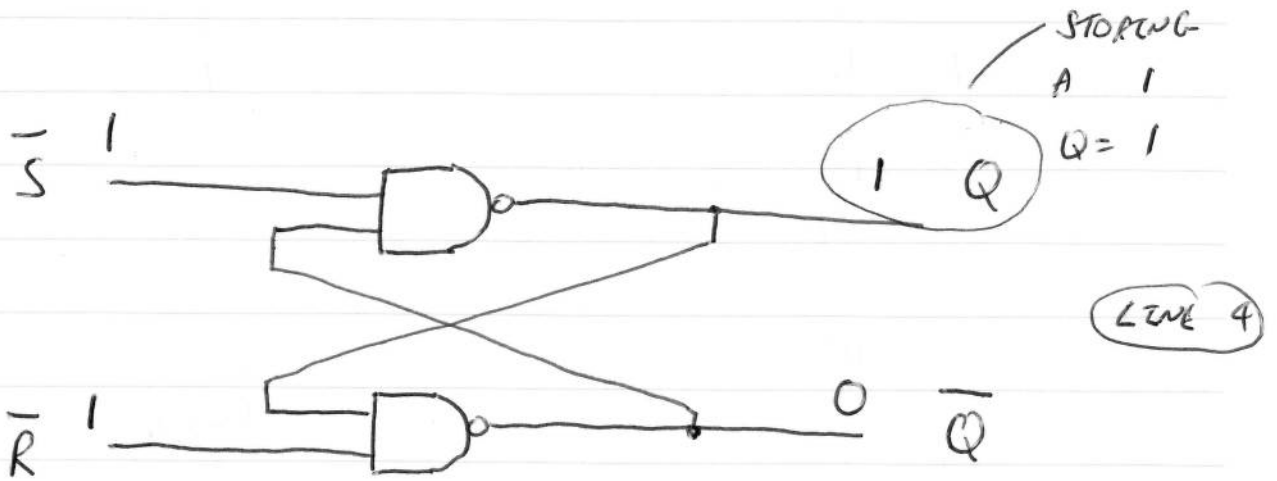
To SET  $Q = 1$   
 $\bar{S} = 0$   $\bar{R} = 1$

SET

To RESET  $Q = 0$   
 $\bar{S} = 1$   $\bar{R} = 0$

RESET

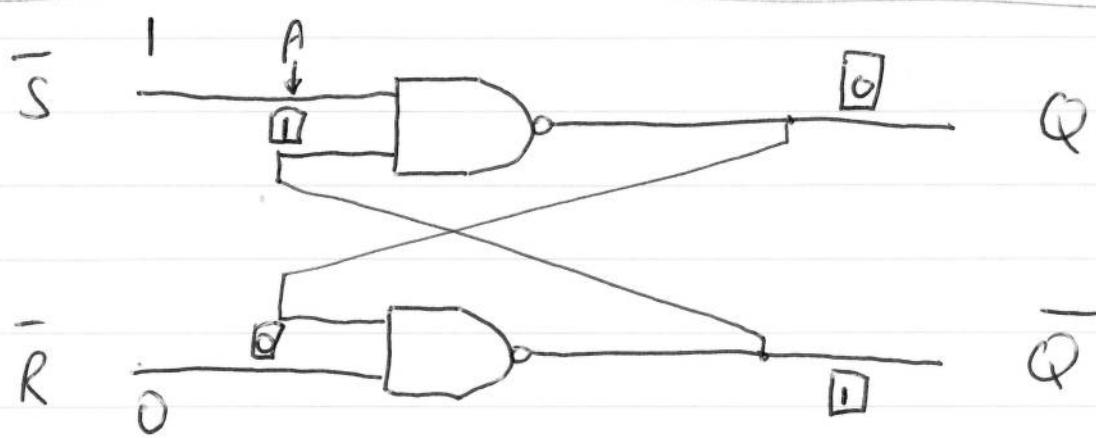
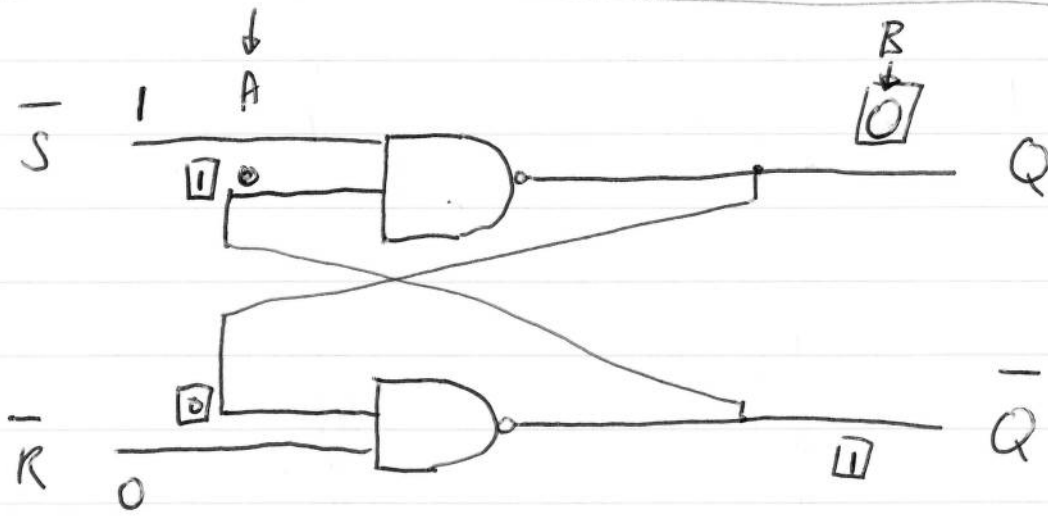
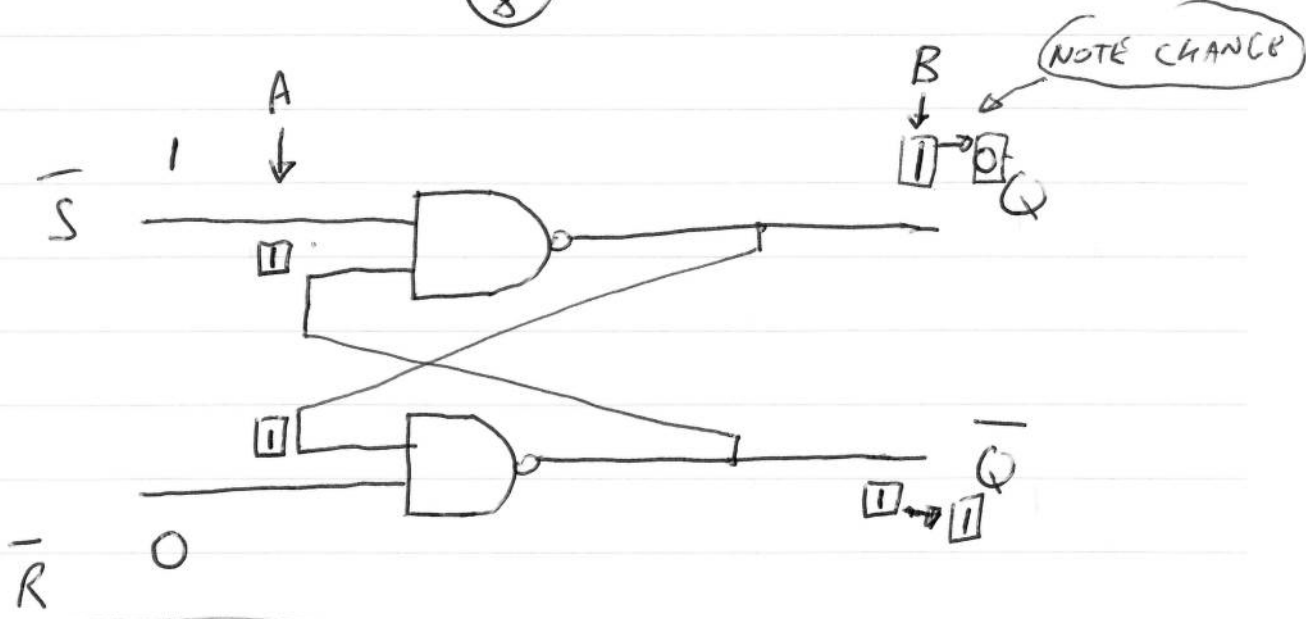
7



How do we RESET ?

$$\bar{S} = 1 \quad \bar{R} = 0$$

8





9

