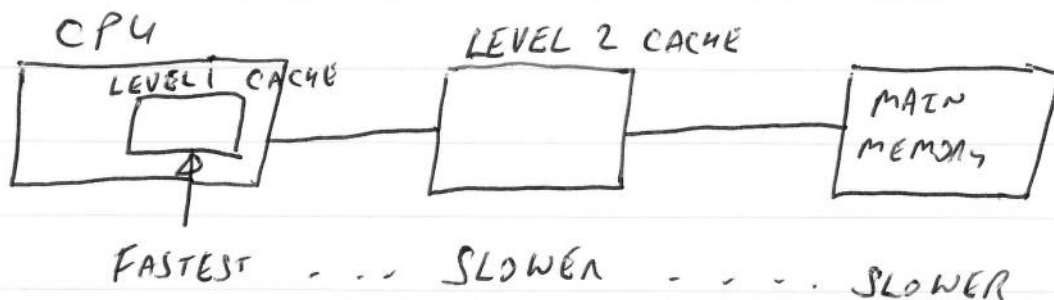


①

CACHE MEMORY

- CPU MEMORY GAP (google)



CPU will look for DATA FIRST OF ALL IN THE LEVEL 1 CACHE.

IF THE DATA IS FOUND THERE THEN WE HAVE A CACHE HIT OTHERWISE \Rightarrow CACHE MISS.

IF WE HAVE A CACHE MISS, THEN WE HAVE TO GO OFF TO ONE OF THE LOWER LEVELS IN THE HIERARCHY.

(2)

CACHE (BLOCKS / CACHE LINES) 0 1 2 3 4 5 6 7

MAIN MEMORY (BLOCKS) 0 1 2 3 4 5 6 7

LOGICAL (BLOCKS) 8 9 10 11 12 13 14 15
16 17 18 19 20 21 22 23
24 25 26 27 28 29 30 31

LI CACHE 64 KB
MAIN MEMORY (RAM) 16 GB

Q. How much bigger is the RAM vs the cache in the above example

$$1 \text{ GB} = 2^{30} \text{ BYTES}$$

$$16 \text{ GB} = 2^{30} \times 16 = 17,179,869,184$$

$$1 \text{ KB} = 2^{10} \text{ BYTES} \quad \underline{\hspace{10em}} = 262144$$

$$64 \text{ KB} = 2^{10} \times 64 = 65536$$

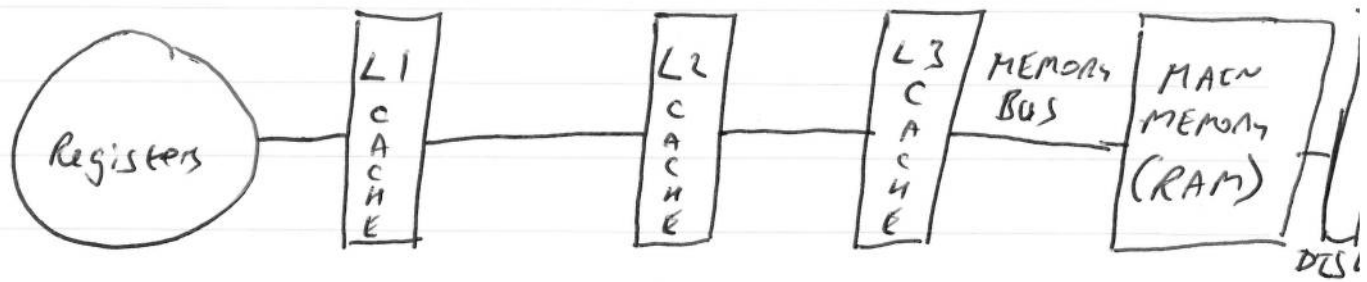
ANSWER: 262144 ^{times} AS MUCH

THE RAM IS 262,144 TIMES THE SIZE OF THE LEVEL 1 CACHE IN THE EXAMPLE

"SPEED VS SIZE"
 S. Memory.pdf

3

Cpu

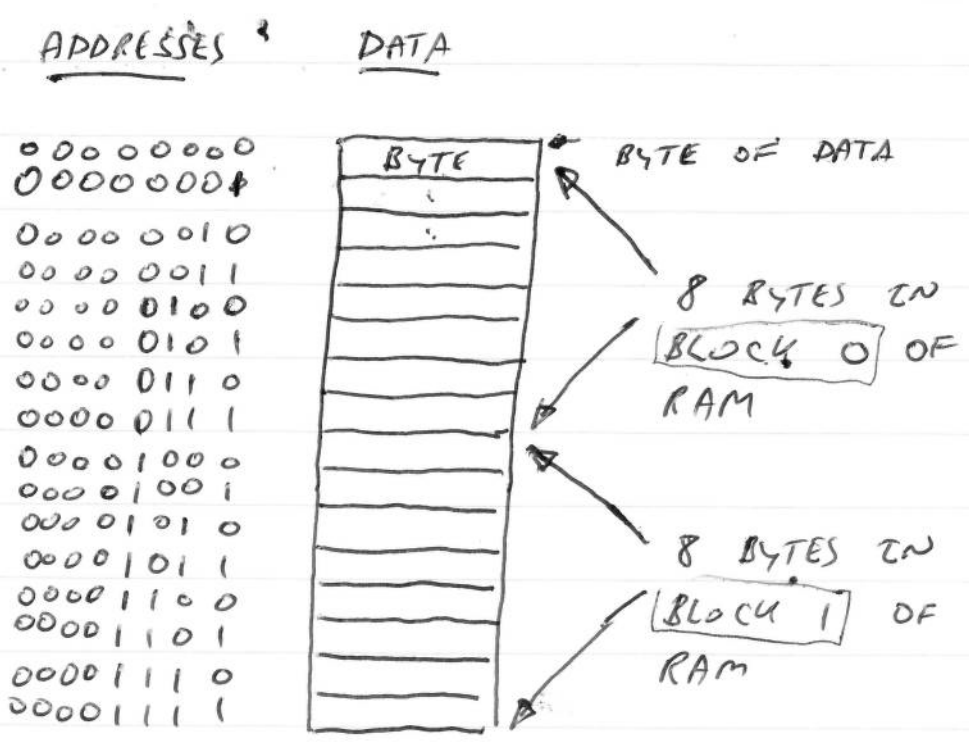


Size	1000 Bytes	64KB	256KB	2-4MB	4-16GB	4-1TB
	300 ps	1 ns	3-10 ns	10-20 ns	500-100 ns	5-1 ms

MAIN MEMORY

Suppose block size is 8 bytes

8



(4)

DIRECT MAPPED CACHE

CACHE : 8 BLOCKS (0..7)
MAIN MEMORY : 32 BLOCKS (0..31)
IN OUR EXAMPLE EACH BLOCK HAS 8 BYTES OF DATA.

CACHE (logical view)

(ADDRESS)
INDEX

VALID
BIT

TAG

DATA
8 BYTES

CACHE
LINE

000
001
010
011
100
101
110
111

VALID BIT	TAG	DATA 8 BYTES

←
"
"
"
←
"
"
"

5

CONSIDER ADDRESS 97 : WHICH BLOCK IN RAM IS ADDRESS 97 IN RAM IN ?

ADDRESS 97 : BLOCK 12 IN RAM
Why is it 12 ?

Block	<u>BLOCKS</u>	IN RAM	<u>BYTES</u>
	0		0.. 7
"	1		8.. 15
"	2		16.. 23
"	3		24.. 31
"	4		32.. 39
"	5		40.. 47
"	6		48.. 55
"	7		56.. 63
"	8		64.. 71
"	9		72.. 79
"	10		80.. 87
"	11		88.. 95
"	12		96.. 103

6

MACHINE WORDS WITH BINARY:
EXPRESS ADDRESS / BYTE 97 in binary.

1100001

RAM

32 BLOCKS OF 8 BYTES

32 x 8 = 256 BYTES

Q. TO PROVIDE 256 DIFFERENT ADDRESSES:
HOW LONG DOES THE ADDRESS BUS NEED TO BE?

A. 8 BITS.

2⁸ = 256

2² = 4 ADDRESSES

E.G. 2 BIT ADDRESS BUS

0	00
1	01
2	10
3	11

2³ = 8 ADDRESSES

3 BIT ADDRESS BUS

0	000
1	001
2	010
3	011
4	100
5	101
6	110
7	111

2⁸ = 256

0	00000000
1	00000001
2	00000010
3	00000011
⋮	
255	11111111

(7)

97: 1100001

97: 01100001

← EIGHT BIT ADDRESS BUS (FOR EXAMPLE)

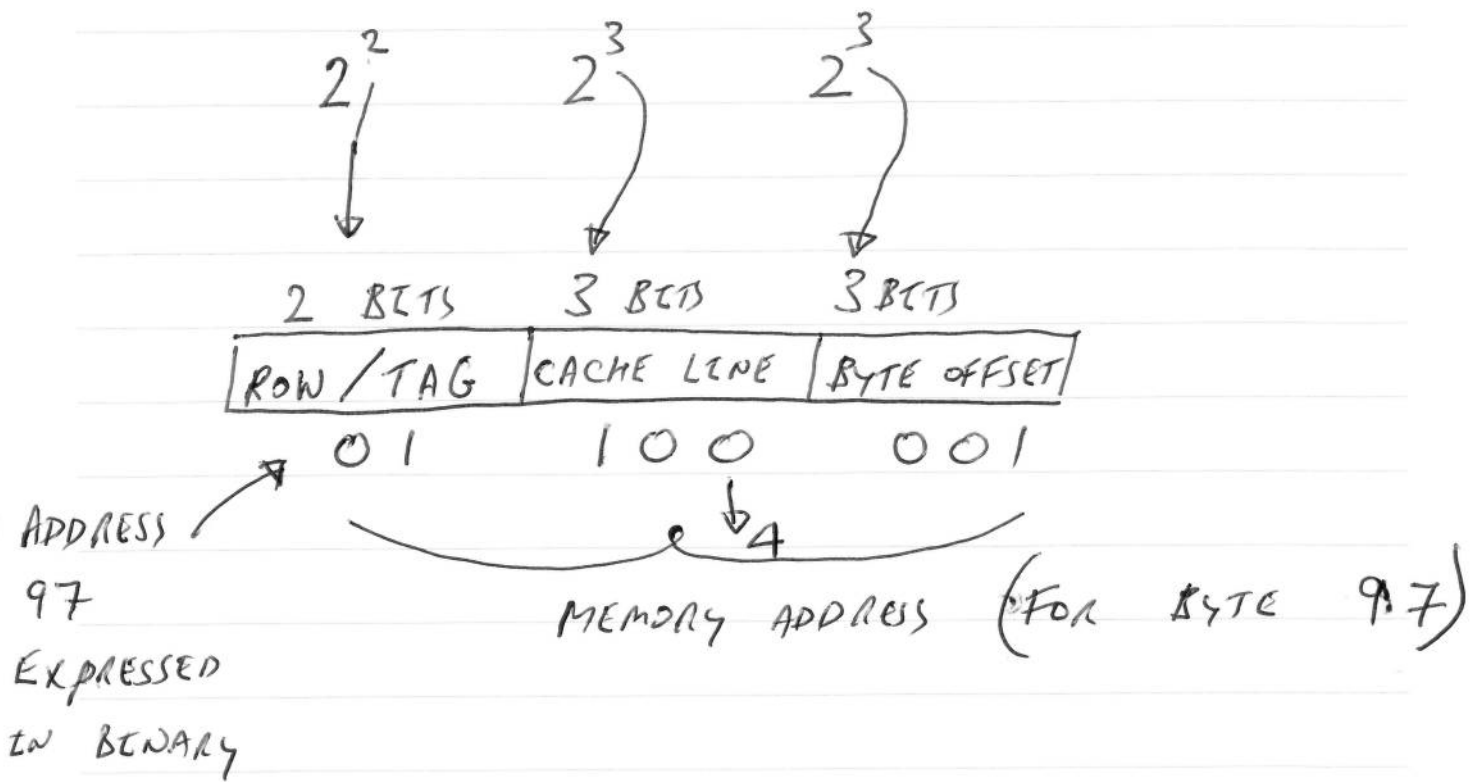
ADDRESS GENERATED: 01100001

	ROWS:	RAM
ROWS: 4	0	0 1 2 3 4 5 6 7
CACHE LINES: 8	1	8 9 10 11 12 13 14 15
BYTES PER BLOCK: 8	2	16 17 18 19 20 21 22 23
	3	24 25 26 27 28 29 30 31

CACHE							
0	1	2	3	4	5	6	7

ROWS: 4	$2^2 = 4$	$2^{(2)} = 4$
CACHE LINES: 8	$2^3 = 8$	$2^{(3)} = 8$
BYTES PER BLOCK (CACHE LINE): 8	$2^3 = 8$	$2^{(3)} = 8$

8



97 IS IN BLOCK 12 IN RAM.

BINARY
100 → BLOCK NO. IN CACHE
4

9

Q What addresses are "in" block 12 in RAM, assuming that each block takes up 8 BYTES.

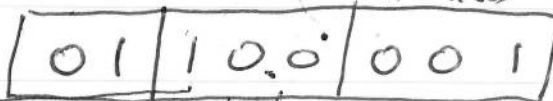
OTHER ASSUMPTIONS: →

CACHE	8 BLOCKS
MAIN MEMORY	32 BLOCKS

A. Ans. ADDRESSES 96 97 98 99 100 101 102 103

CPU GENERATES THE FOLLOWING ADDRESS

97 :



TAG / INDEX BYTE
 ROW CACHE LINE OFFSET

WHERE IN THE CACHE WILL BLOCK 12 go?

INDEX	VALID BIT	TAG	DATA							
			2	3	4	5	6	7		
000	0									
001	0									
010	0									
011	0									
100	1	01	96	97	98	99	100	101	102	103
101	0									
110	0									
111	0									

(CACHE MISS)

3 THINGS DONE (FIRSTLY FIND LINE 100 IN CACHE)

- (A) SET VALID BIT TO 1
- (B) SET TAG TO 01
- (C) COPY DATA FROM BYTES 96 TO 103 IN RAM, INTO CACHE

WAS THIS A CACHE HIT OR A CACHE MISS ?

A. MISS. IT TAKES TIME TO COPY DATA FROM ONE LEVEL OF THE MEMORY HIERARCHY INTO ANOTHER. (IN THIS CASE FROM BLOCK 12 IN RAM INTO BLOCK 4 IN CACHE)

(CACHE HIT)

CPU GENERATES A REQUEST FOR BYTE 101
8 BIT BINARY ADDRESS
101 → 01100101

THU 10 MAR 2016

(i)

		000	001	010	011	100	101	110	111
		0	1	2	3	4	5	6	7
TAG/ROW									
0	00	0	1	2	3	4	5	6	7
1	01	8	9	10	11	12	13	14	15
2	10	16	17	18	19	20	21	22	23
3	11	24	25	26	27	28	29	30	31

EACH OF THE ABOVE IS A BLOCK.

WE ASSUMED 8 BYTES PER BLOCK.

How MANY BYTES IN THE RAM ABOVE?

32 BLOCKS x 8 BYTES = 256

TAKE ADDRESS 77.

Which block is it in ?

$$8 \overline{) 77} \\ \underline{9 \text{ R } 5}$$

77 CONVERTED TO BINARY

01001101

TAG	INDEX	BYTE OFFSET
01	001	101

②

Consider Block 9.

What is the addresses of the other bytes in block 9 ?



OTHER ADDRESSES IN BLOCK 9

72	01001000
73	01001001
74	01001010
75	01001011
76	01001100
77	01001101
78	01001110
79	01001111

ADDRESS

8 | 77

BLOCK 9 R 5

$9 \times 8 = 72$

72 is 1st ADDRESS
IN BLOCK 9

(3)

EXERCISE

TAKE ADDRESS 180

What block is it in ?

What are the addresses of the other bytes in the block ?

(EXPRESSED IN BOTH BINARY AND DECIMAL)

What block is it in ?

$$8 \overline{) 180}$$

22 R 4

Block 22 ~~INDEX~~ ~~4~~ BYTE OFFSET: 4

180 = TAG, INDEX, BYTE OFFSET
1 0 1 1 0 1 0 0

176	1 0 1 1 0 1 0 0
177	1 0 1 1 0 1 0 1
178	1 0 1 1 0 1 0 1 0
179	1 0 1 1 0 1 0 1 1
180	1 0 1 1 0 1 0 0
181	1 0 1 1 0 1 0 1
182	1 0 1 1 0 1 1 0
183	1 0 1 1 0 1 1 1

ADDRESS

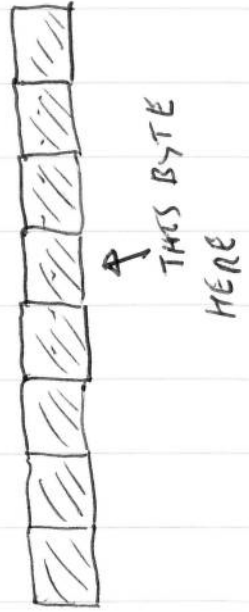
8 $\overline{) 180}$

BLOCK: 22 R 4

$22 \times 8 = 176$

176 IS 1ST ADDRESS
IN BLOCK 22

(4)



000 001 010 011 100 101 110 111

0 1 2 3 4 5 6 7

TAG/ROW	00	0	1	2	3	4	5	6	7
0	00	0	1	2	3	4	5	6	7
1	01	8	9	10	11	12	13	14	15
2	10	16	17	18	19	20	21	22	23
3	11	24	25	26	27	28	29	30	31

BLOCK 22 ABOVE